FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 500125.02	APPLICATION NO. 10/007,871
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*		APPLICANT(S) Brian W. Huber et al.	
		FILING DATE November 13, 2001	GROUP ART UNIT 2818

U.S. PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC				Viet Q. Nguyen Primary Examiner		
	AD						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	AE			Viet Q. Nguyen Primary Examiner				
	AF							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

✓	AG	Chinnery, D.G. et al., *"Achieving 550 MHz in an ASIC Methodology,"* Department of EECS, University of California at Berkeley, 2001, 6 pages.
✓	AH	Friedman, Eby G., *"Clock Distribution Networks in Synchronous Digital Integrated Circuits,"* Invited Paper, IEEE, Vol. 89, No. 5, May 2001, pp. 665-692.
✓	AI	Friedman, Eby G. et al., *"Design and Analysis of a Hierarchical Clock Distribution System for Synchronous Standard Cell/Macrocell VLSI,"* IEEE, Vol. SC-21, No. 2, April 1986, pp. 240-246.
✓	AJ	IBM, *"Method of Deskewing Data Pulses,"* IBM Technical Disclosure Bulletin, Vol. 28, No. 6, November 1985, pp. 2658-2659.
✓	AK	Montanaro, James et al., *"A 160-MHz, 32-b, 0.5-W CMOS RISC Microprocessor,"* IEEE, Vol. 31, No. 11, November 1996, pp. 1703-1714.
✓	AL	Nomura, Masahiro et al., *"A 300-MHz 16-b, 0.5-µm BiCMOS Digital Signal Processor Core LSI,"* IEEE, Vol. 29, No. 3, March 1994, pp. 290-297.
✓	AM	Tokumaru, Takeji et al., *"Design of a 32-bit Microprocessor, TX1,"* IEEE, Vol. 24, No. 4, August 1989, pp. 938-944.

EXAMINER *V. Nguyen* Viet Q. Nguyen Primary Examiner	DATE CONSIDERED 3/21/04

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).